Appl. No. 10/707,860

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Amdt. dated September 26, 2006

Reply to Office action of June 29, 2006

Amendments to the Specification:

Please replace paragraph [0027] with the following amended paragraph:

Please refer to Fig.2 showing a block diagram of the second embodiment of the

extendable computer system according to the present invention. The present invention

provides an extendable computer system 200 comprising a motherboard 210 for

maintaining the functionality of the computer system 200. The motherboard 210

comprises a system chipset 214, which is a North Bridge chipset or a South Bridge

chipset in this embodiment, for supporting a specification of a first interface, which is a

PCI Express interface in this embodiment. The motherboard 210 further comprises at

least one first standard communication interface slot (not shown in Fig.2) 215 electrically

connected to the system chipset 214 for electrically connecting at least one peripheral

device to the computer system 200. The motherboard 210 further comprises a first

extending port 212 electrically connected to the system chipset 214 for extending

15 functionality of the motherboard 210.

Please replace paragraph [0030] with the following amended paragraph:

In this embodiment, the extended board 220 further comprises at least one switching

circuit 225, for performing Hot-Plug control and power management of the at least one

second standard communication interface slot 226, electrically connected to the second

interface converter 224 and the at least one second standard communication interface slot

226. The extended board 220 further comprises at least one switching button (not shown

in Fig.2) 227, such as a manual switch or a push-button, electrically connected to the at

least one switching circuit 225 for controlling the at least one switching circuit 225 to

switch on or switch off the protocol, data signals, and power of the at least one second

standard communication interface slot 226. The installation of the above-mentioned at

least one switching circuit 225 and the corresponding switching button 227 of the second

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embodiment is a choice of implementation of the present invention, and thus, not a limitation.

Please replace paragraph [0032] with the following amended paragraph:

The first extending port 212 further comprises at least one first power management end set (not shown in Fig.2) 213A electrically connected to the power management chipset, such as the afore-mentioned South Bridge chipset, of the computer system 200 for transmitting at least one power management signal and at least one monitoring signal. The extended board 220 further comprises at least one switching circuit 225 electrically connected to the second interface converter 224 and the at least one second standard communication interface slot 226 for performing Hot-Plug control and power management of the at least one second standard communication interface slot 226. The at least one switching circuit 225 is also electrically connected to the at least one first power management end set 213A through at least one second power management end set (not shown in Fig.2) 223A of the second extending port 222. Please note that the power management chipset is capable of controlling and monitoring the Hot-Plug control and the power management of the at least one second standard communication interface slot 226 with the at least one switching circuit 225, and the second interface converter 224 is capable of controlling the at least one switching circuit 225 with a protocol of the second interface to switch on or switch off the protocol and data signals of the at least one second standard communication interface slot 226 when receiving the at least one power management signal. Also, the installation of the above-mentioned at least one switching circuit 225 and the corresponding switching button 227 of the second embodiment, together with the related variance of the third embodiment, are choices of implementation of the present invention, and thus, not a limitation.

Please replace paragraph [0034] with the following amended paragraph:

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The first extending port 212 further comprises at least one first monitoring end (not shown in Fig.2) 213B electrically connected to a power management chipset, such as the afore-mentioned South Bridge chipset, of the computer system 200 for transmitting at least one Hot-Plug/Power Switch monitoring signal. The extended board 220 further comprises at least one switching circuit 225 electrically connected to the second interface converter 224 and the at least one second standard communication interface slot 226 for performing Hot-Plug control and power management of the at least one second standard communication interface slot 226. The at least one switching circuit 225 is also electrically connected to the at least one first monitoring end 213B through at least one second monitoring end (not shown in Fig.2) 223B of the second extending port 222. In addition, the second interface converter 224 is capable of controlling the at least one switching circuit 225 with a protocol of the second interface to switch on or switch off the protocol, data signals, and power of the at least one second standard communication interface slot 226.